

Prior Art

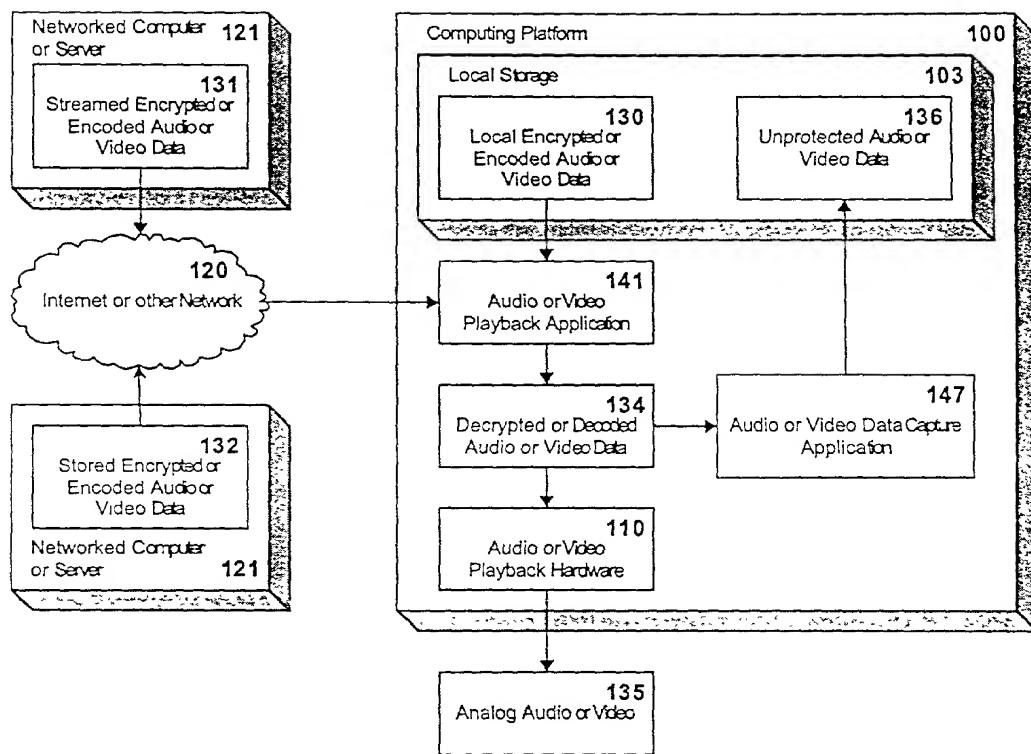


Figure 1

Prior Art

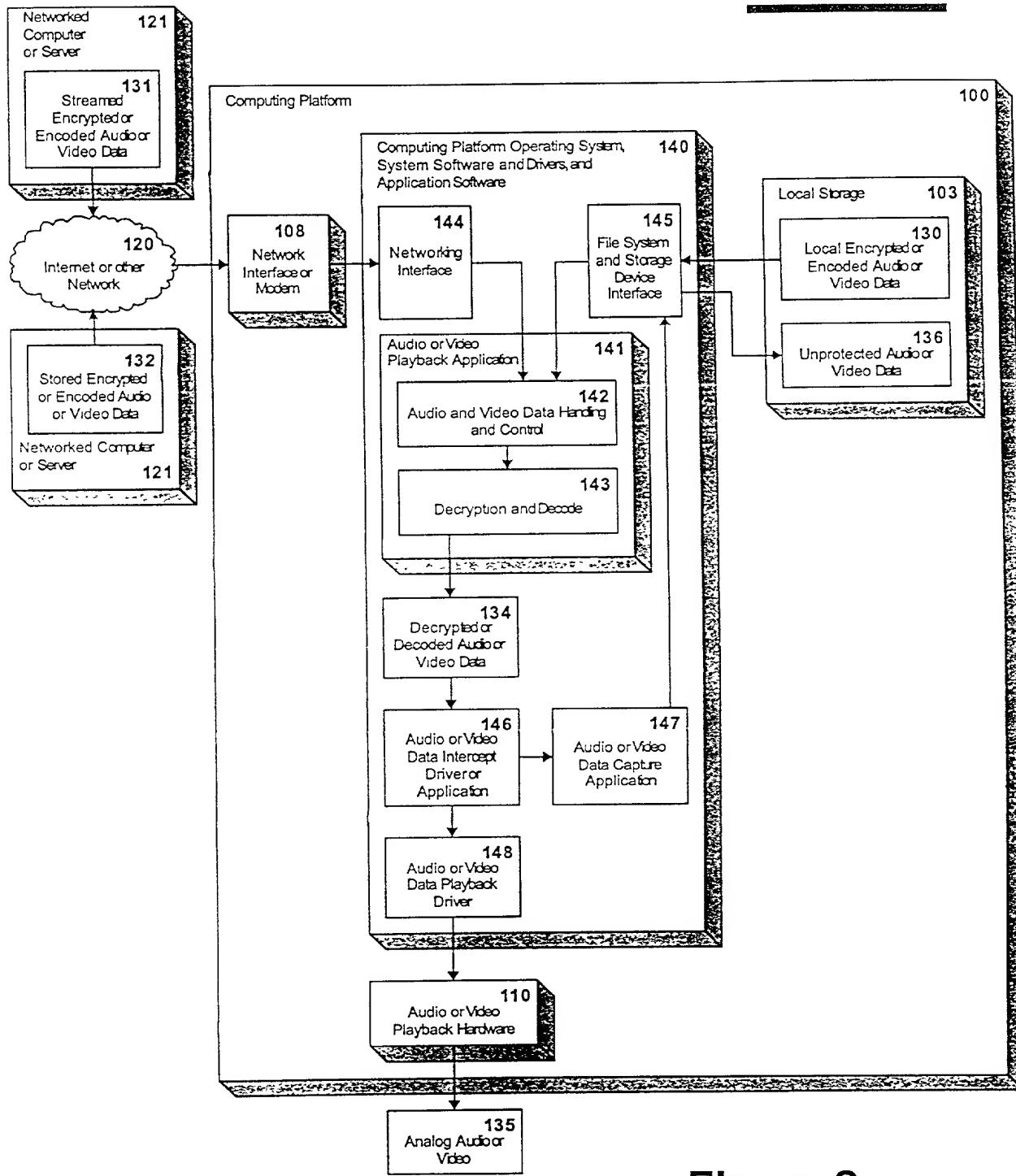


Figure 2

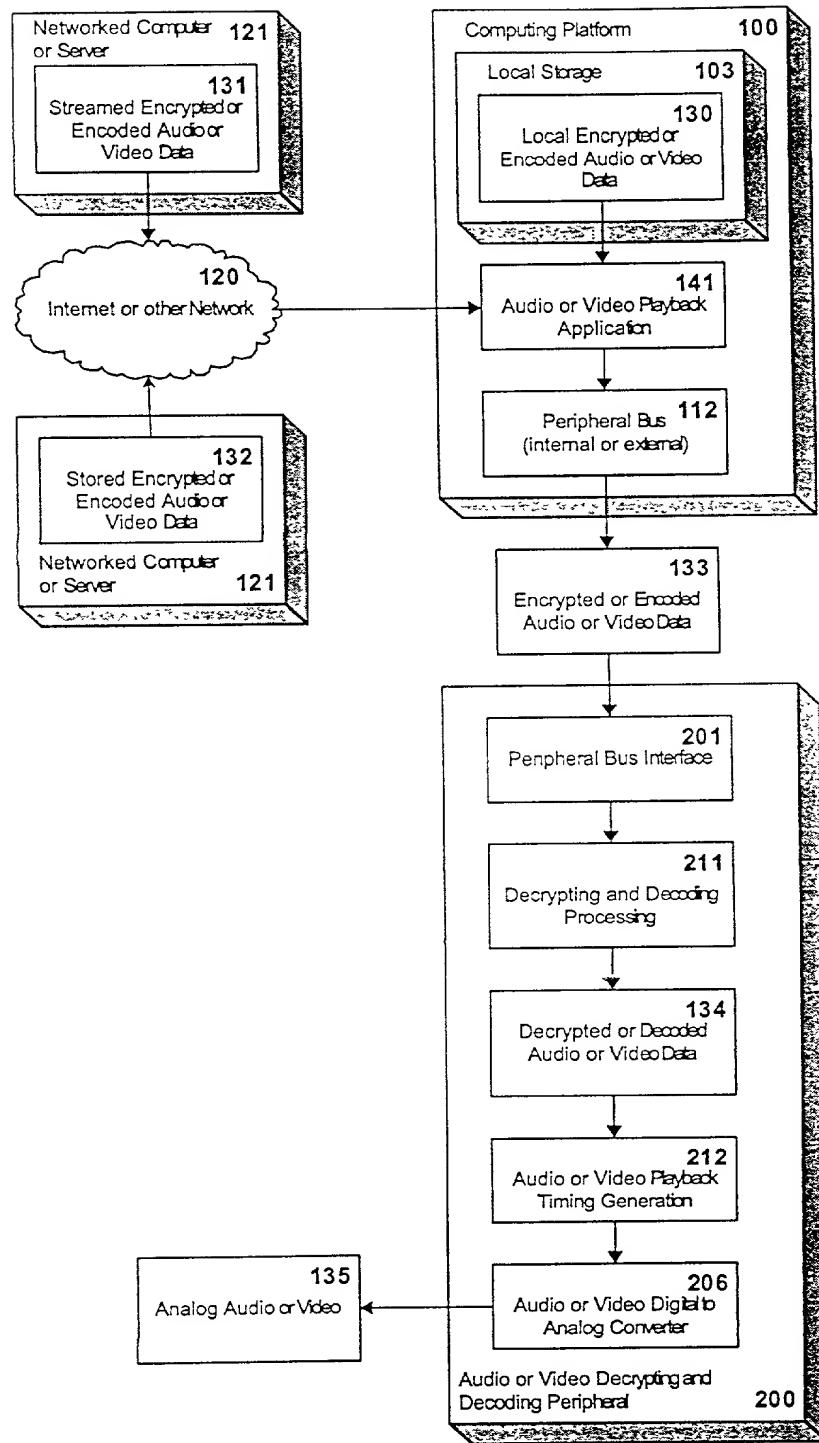


Figure 3

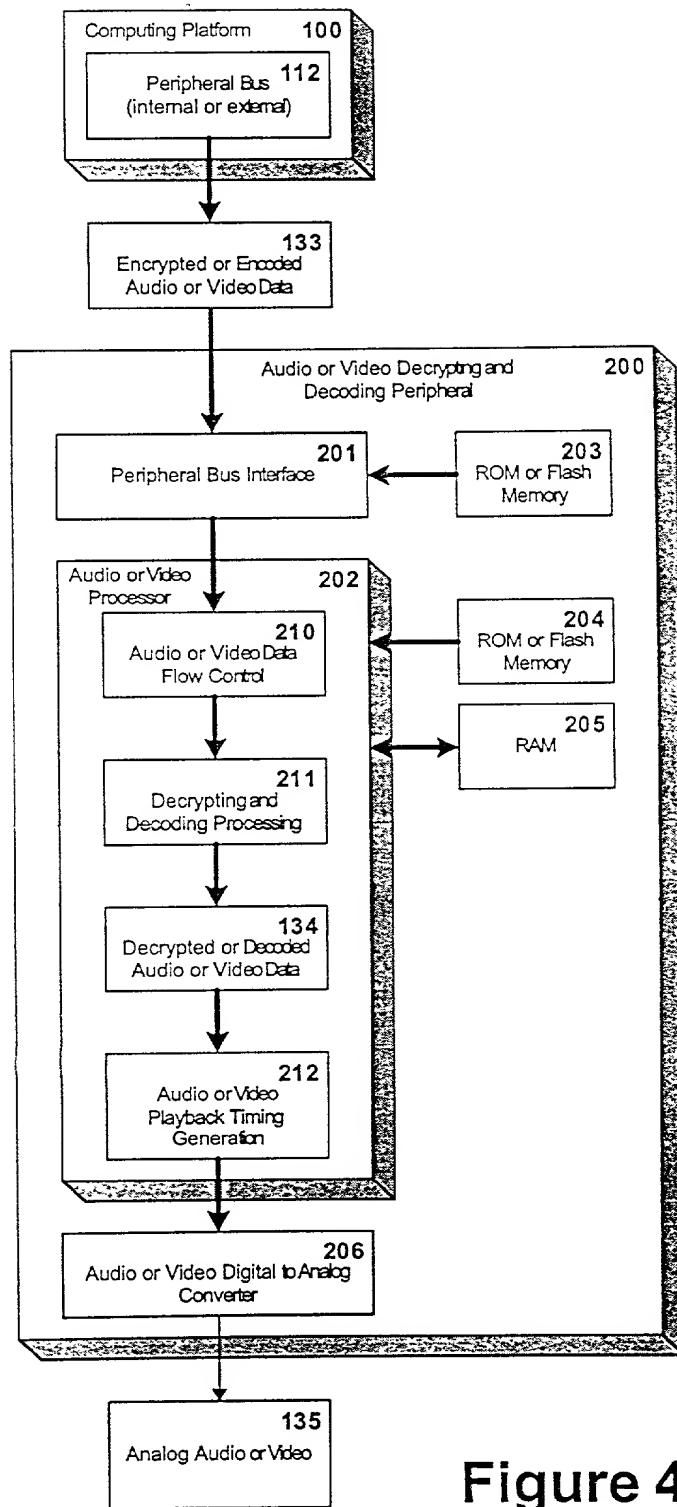


Figure 4

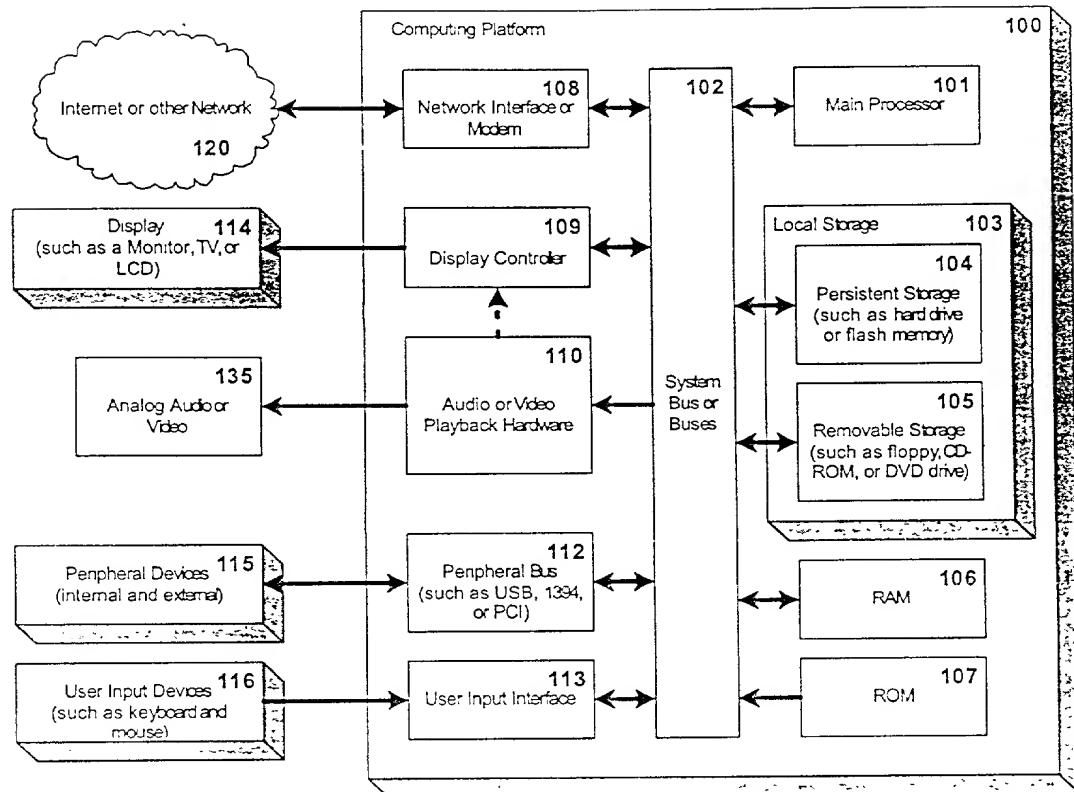


Figure 5

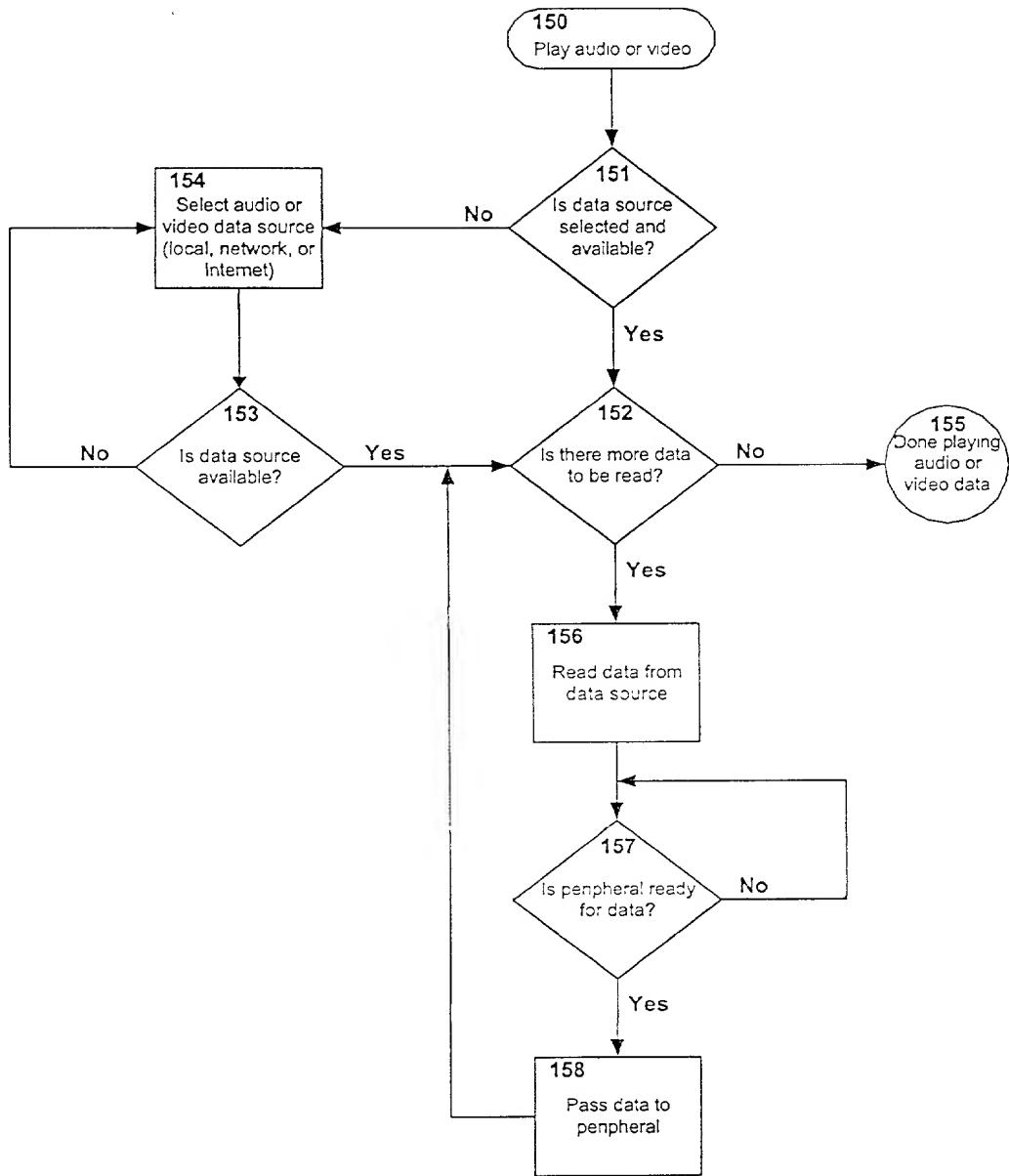


Figure 6

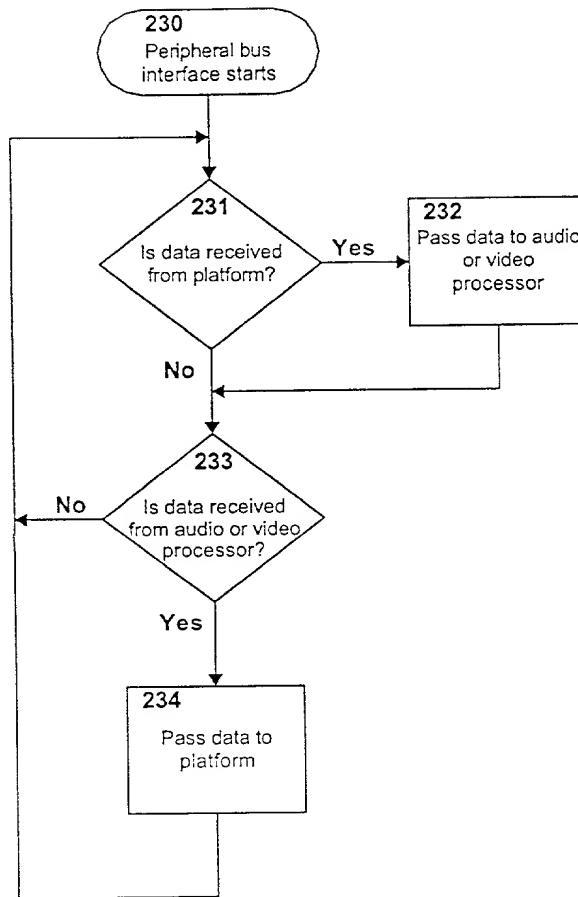


Figure 7

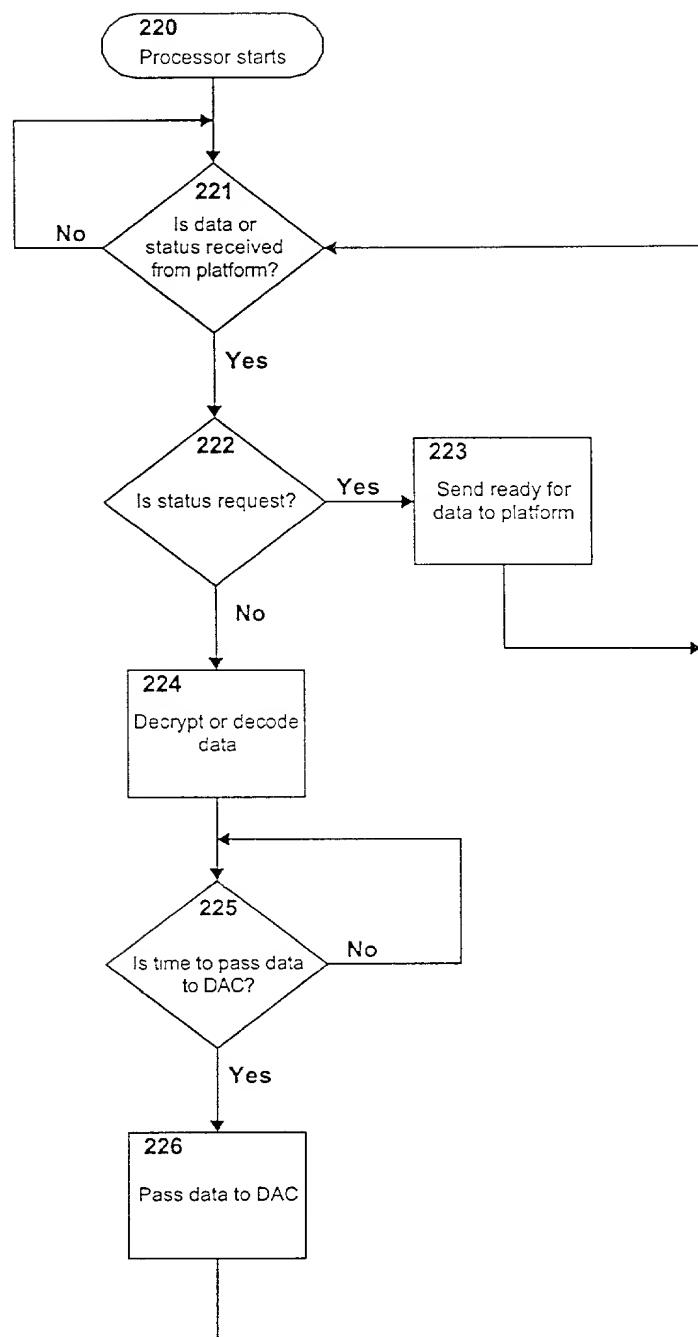


Figure 8

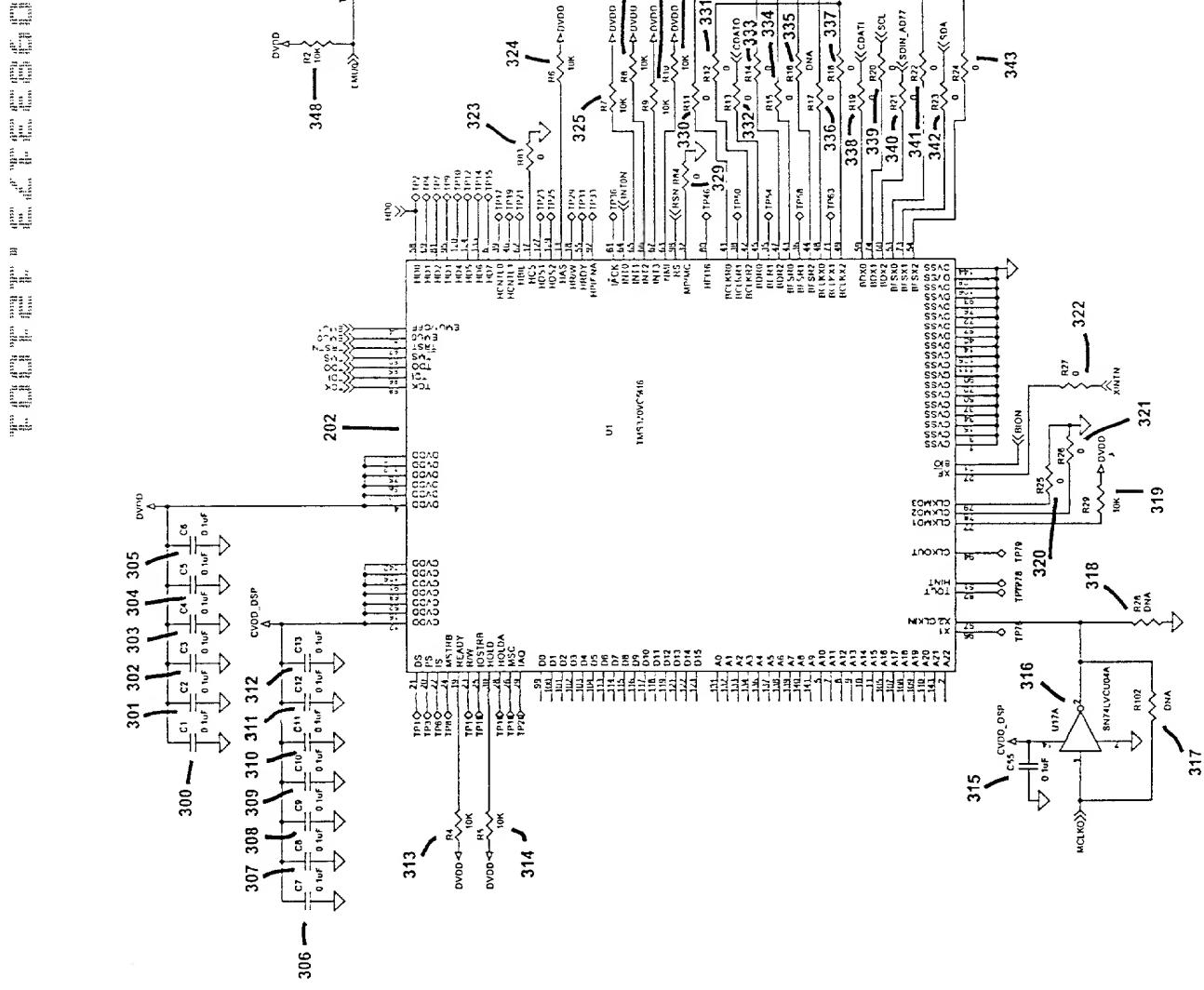


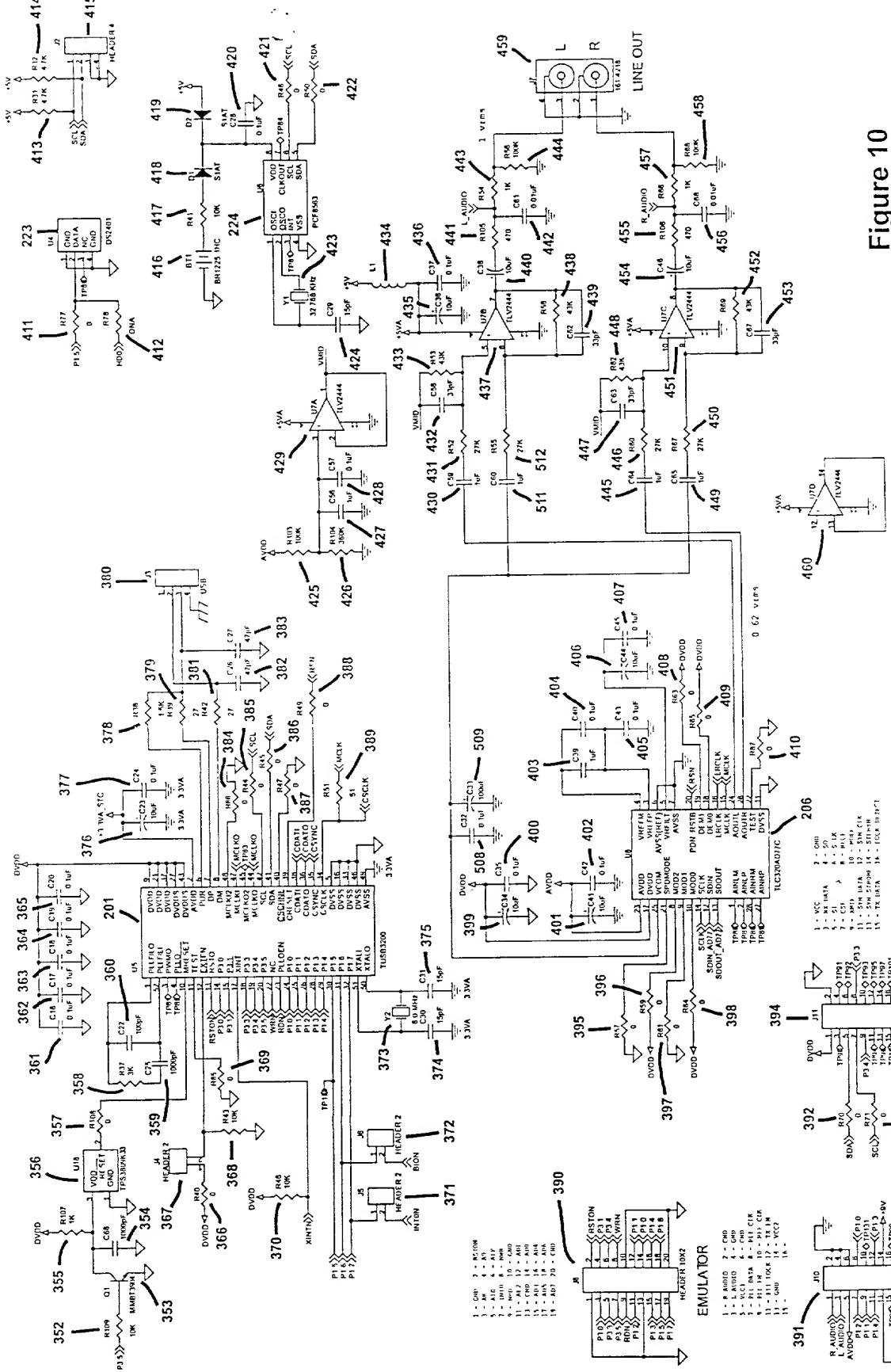
Figure 9

TRANSMITTER

EMULATOR

LINE OUT

Figure 10



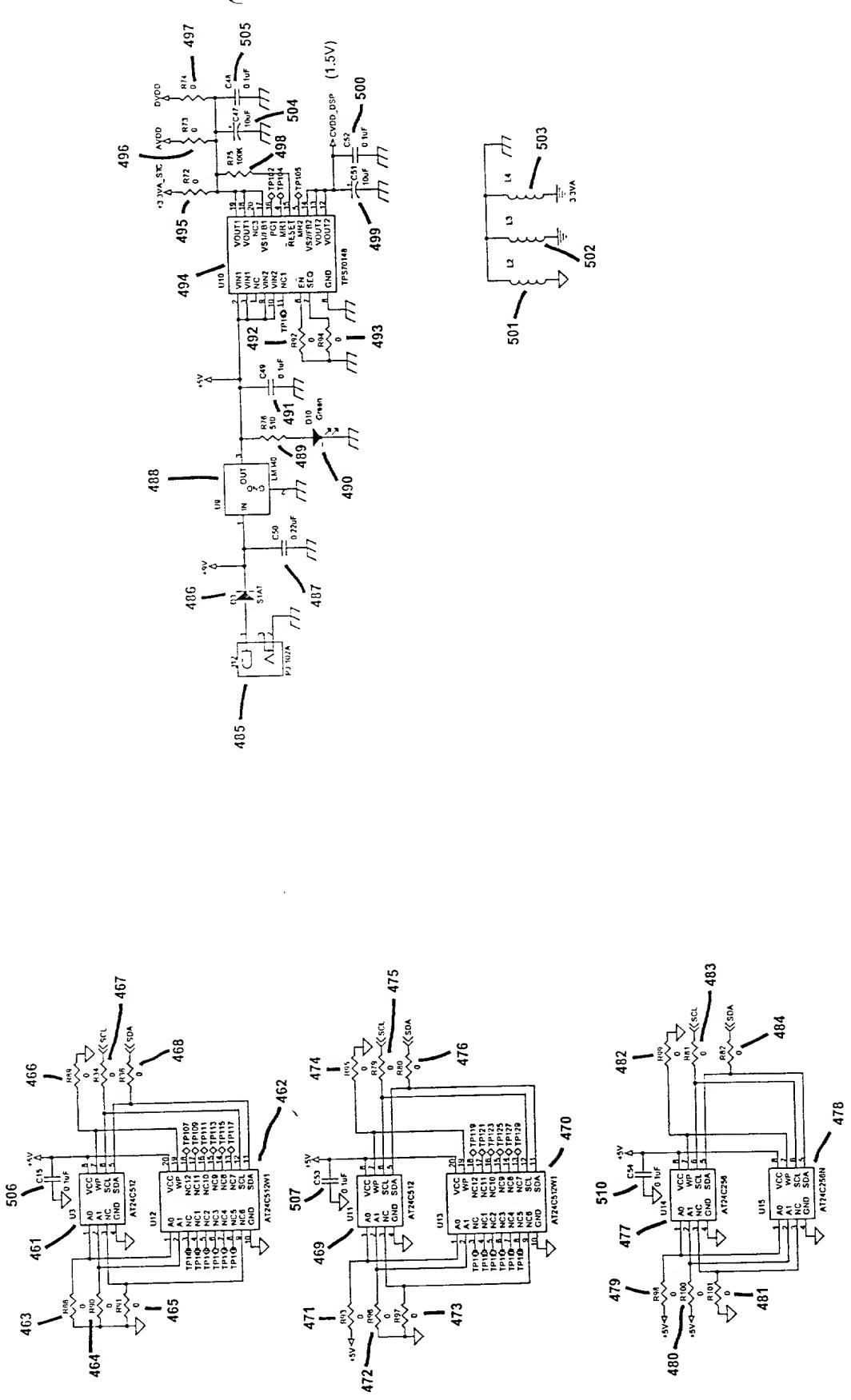


Figure 11